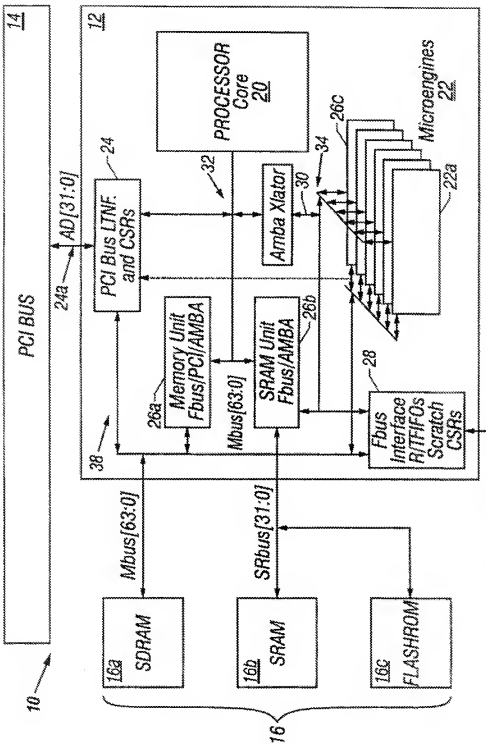
10
PCI BUS
14
12
AD[31:0]
24a
PCI Bus LTNF. and CSRs
24
PROCESSOR Core 20
32
Amba Xlator
30
34
26c
Microengines 22
22a
Memory Unit Fbus/PCI/AMBA
26a
SRAM Unit Fbus/AMBA
26b
Mbus[63:0]
28
Fbus Interface R/TFIFOs Scratch CSRs
38
Mbus[63:0]
SRbus[31:0]
FBUS [63:0]
18
16a SDRAM
16b SRAM
16c FLASHROM
16
10/100 Base T Octal MAC
13a
Gigabit Ethernet
13b

FIG. 1